

## STPS16170C

## High voltage power Schottky rectifier

### Main product characteristics

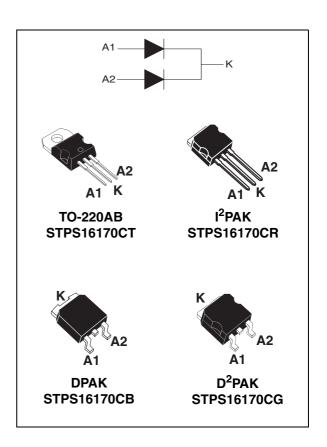
I <sub>F(AV)</sub>	2 x 8 A
$V_{RRM}$	170 V
T <sub>j</sub>	175° C
V <sub>F</sub> (typ)	0.70 V

### Features and benefits

- High junction temperature capability
- Good trade-off between leakage current and forward voltage drop
- Low leakage current
- Avalanche capability specified

### **Description**

Dual centre tab Schottky rectifier designed for high frequency switch mode power supplies.



### **Order codes**

Part Number	Marking
STPS16170CT	STPS16170CT
STPS16170CG	STPS16170CG
STPS16170CG-TR	STPS16170CG
STPS16170CR	STPS16170CR
STPS16170CB-TR	PS16170CB
STPS16170CB	PS16170CB

**Characteristics** STPS16170C

#### **Characteristics** 1

Table 1. Absolute ratings (limiting values per diode,  $T_{amb} = 25^{\circ}$  C unless otherwise specified)

Symbol	Parameter			Value	Unit
$V_{RRM}$	Repetitive peak reverse voltage			170	V
I <sub>F(RMS)</sub>	RMS forward current			20	Α
-	Average forward current, $\delta = 0.5$ $T_c = 150^{\circ}  \text{C}$ Per diode		8	Α	
I <sub>F(AV)</sub>			Total package	16	A
I <sub>FSM</sub>	Surge non repetitive forward current	t <sub>p</sub> = 10 ms Sinus	t <sub>p</sub> = 10 ms Sinusoidal		
P <sub>ARM</sub>	Releative peak avalanche power	nnche power $T_j = 25^{\circ} \text{ C}$ $t_p = 1 \mu \text{s}$			W
T <sub>stg</sub>	Storage temperature range			-65 to + 175	°C
Tj	Maximum operating junction temperature <sup>(1)</sup>			175	°C
dV/dt	Critical rate of rise of reverse voltage			10 000	V/µs

 $<sup>\</sup>frac{dP_{tot}}{dT_{j}} < \frac{1}{R_{th(j-a)}}$ 1 thermal runaway condition for a diode on its own heatsink

Table 2. Thermal parameters

Symbol	Parameter	Value	Unit	
В	lunction to cook	Per diode	3	
R <sub>th(j-c)</sub>	Junction to case	Total	1.8	°C/W
R <sub>th(c)</sub>	Coupling		0.6	

Static electrical characteristics Table 3.

Symbol	Parameter	Test conditions		Min.	Тур	Max.	Unit
I_(1)	I <sub>R</sub> <sup>(1)</sup> Reverse leakage current	T <sub>j</sub> = 25° C	$V_R = V_{RRM}$			15	μΑ
'R`		T <sub>j</sub> = 125° C				15	mA
		T <sub>j</sub> = 25° C			0.92		
V (2)	V <sub>F</sub> <sup>(2)</sup> Forward voltage drop	T <sub>j</sub> = 125° C	- 0 A		0.70	0.75	V
VF.		T <sub>j</sub> = 25° C	I <sub>F</sub> = 16 A			1	V
		T <sub>j</sub> = 125° C	1F = 10 A		0.8	0.86	

<sup>1.</sup> Pulse test:  $t_p$  = 5 ms,  $\delta$  < 2 %

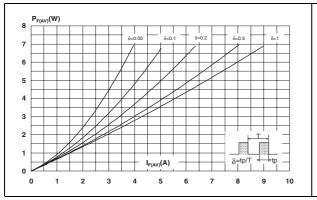
To evaluate the conduction losses use the following equation: P = 0.64 x  $I_{F(AV)}$  + 0.014 x  $I_{F}^{2}_{(RMS)}$ 

$$P = 0.64 \times I_{F(AV)} + 0.014 \times I_{F(RMS)}^{2}$$

<sup>2.</sup> Pulse test:  $t_p$  = 380  $\mu$ s,  $\delta$  < 2 %

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Figure 1. Conduction losses versus average Figure 2. Average forward current versus ambient temperature ( $\delta$  = 0.5, per diode)



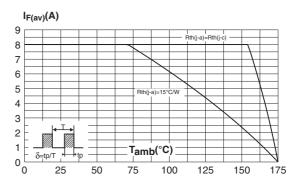
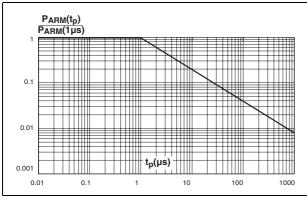


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature



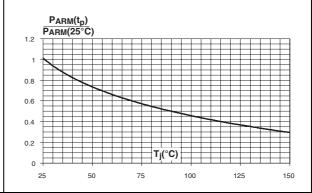
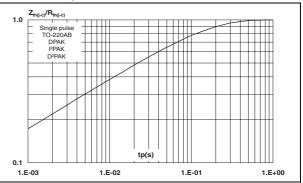


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode)

120 I<sub>M</sub>(A)
100
80
60
40
100
1.E-03
1.E-02
1.E-01
1.E+00

Figure 6. Relative variation of thermal impedance junction to case versus pulse duration



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Figure 7. Reverse leakage current versus reverse voltage applied (typical values, per diode)

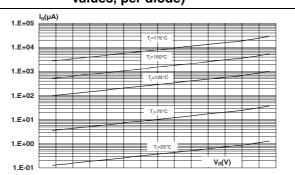


Figure 8. Junction capacitance versus reverse voltage applied (typical values, per diode)

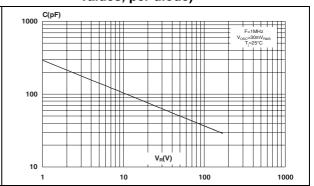
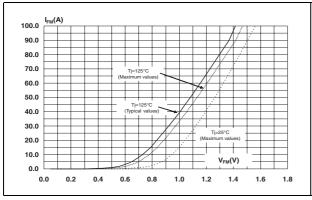


Figure 9. Forward voltage drop versus forward current (per diode)

Figure 10. Thermal resistance junction to ambient versus copper surface under tab (epoxy printed board FR4, Cu = 35 µm - DPAK)



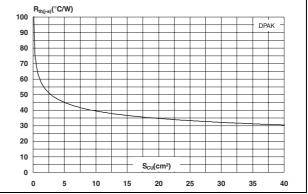
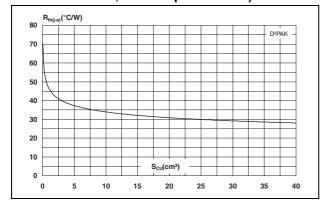


Figure 11. Thermal resistance junction to ambient versus copper surface under tab (epoxy printed board FR4, Cu = 35 µm - D<sup>2</sup>PAK)

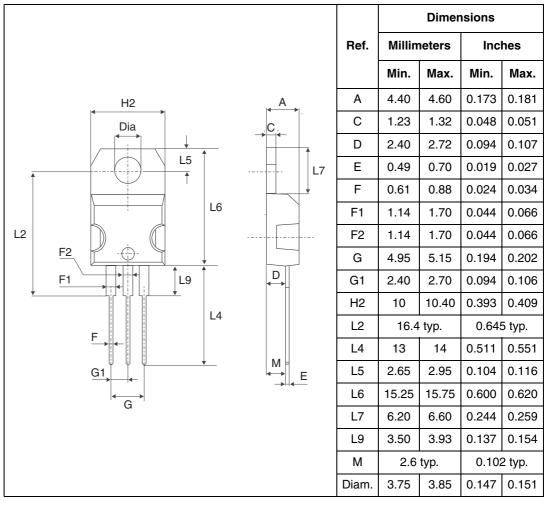


STPS16170C Package information

## 2 Package information

Epoxy meets UL94, V0

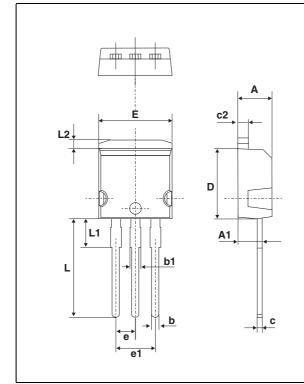
Table 4. T0-220AB dimensions



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Package information STPS16170C

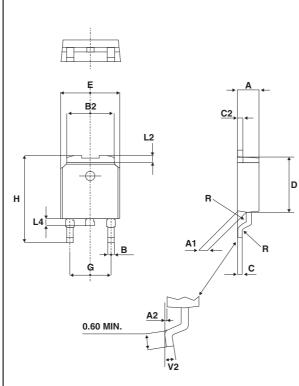
Table 5. I<sup>2</sup>PAK dimensions



	Dimensions			
Ref.	Millimeters		Inches	
	Min.	Max.	Min.	Max.
Α	4.40	4.60	0.173	0.181
A1	2.40	2.72	0.094	0.107
b	0.61	0.88	0.024	0.035
b1	1.14	1.70	0.044	0.067
С	0.49	0.70	0.019	0.028
c2	1.23	1.32	0.048	0.052
D	8.95	9.35	0.352	0.368
е	2.40	2.70	0.094	0.106
e1	4.95	5.15	0.195	0.203
Е	10	10.40	0.394	0.409
L	13	14	0.512	0.551
L1	3.50	3.93	0.138	0.155
L2	1.27	1.40	0.050	0.055

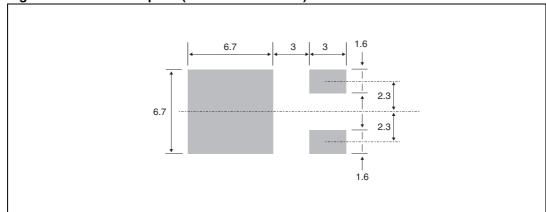
STPS16170C Package information

Table 6. DPAK dimensions



	Dimensions				
Ref.	Millimeters		Inches		
	Min.	Max	Min.	Max.	
Α	2.20	2.40	0.086	0.094	
A1	0.90	1.10	0.035	0.043	
A2	0.03	0.23	0.001	0.009	
В	0.64	0.90	0.025	0.035	
B2	5.20	5.40	0.204	0.212	
С	0.45	0.60	0.017	0.023	
C2	0.48	0.60	0.018	0.023	
D	6.00	6.20	0.236	0.244	
Е	6.40	6.60	0.251	0.259	
G	4.40	4.60	0.173	0.181	
Н	9.35	10.10	0.368	0.397	
L2	0.80 typ.		0.03	I typ.	
L4	0.60	1.00	0.023	0.039	
V2	0°	8°	0°	8°	

Figure 12. DPAK footprint (dimensions in mm)



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Table 7. D<sup>2</sup>PAK dimensions

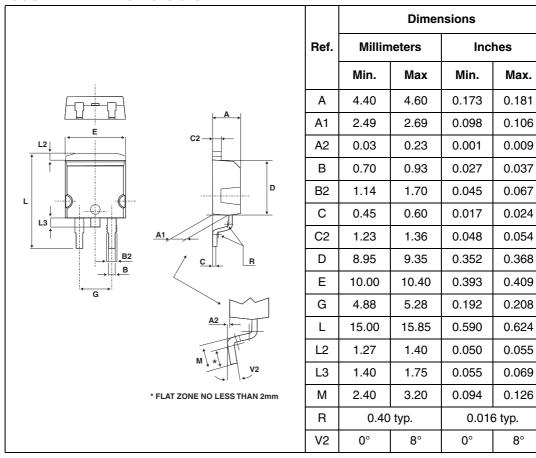
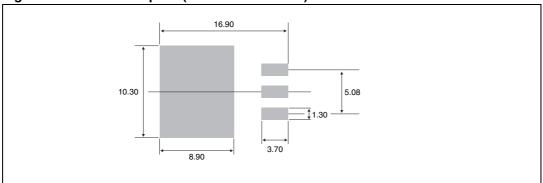


Figure 13. D<sup>2</sup>PAK footprint (dimensions in mm)



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

# 3 Ordering information

Part Number	Marking	Package	Weight	Base qty	Delivery mode
STPS16170CT	STPS16170CT	TO-220ABB	2.23 g	50	Tube
STPS16170CG	STPS16170CG	D <sup>2</sup> PAK	1.48 g	50	Tube
STPS16170CG-TR	STPS16170CG	D <sup>2</sup> PAK	1.48 g	1000	Tape and reel
STPS16170CR	STPS16170CR	I <sup>2</sup> PAK	1.49 g	50	Tube
STPS16170CB-TR	PS16170CB	DPAK	0.3 g	2500	Tape and reel
STPS16170CB	PS16170CB	DPAK	0.3 g	75	Tube

## 4 Revision history

Date	Revision	Description of Changes
13-Jul-2006	1	First issue

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